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FORM PTO-1449

ATTY. DOCKET NO.
1778.0210000APPLICATION NO.
09/882,285FIRST NAMED INVENTOR
Kevin D. KissellFILING DATE
June 18, 2001ART UNIT
2111FIRST SUPPLEMENTAL
INFORMATION DISCLOSURE STATEMENT

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB- CLASS	FILING DATE
	AA2						
	AB2						
	AC2						
	AD2						
	AE2						
	AF2						
	AG2						
	AH2						
	AI2						
	AJ2						
	AK2						

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FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLATION
	AL2						Yes No
	AM2						Yes No
	AN2						Yes No
	AO2						Yes No
	AP2						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

	AR	2	
	AS	2	
CSK	AT	2	ARM Architecture Reference Manual, Advanced RISC Machines Ltd., Prentice Hall, pp. 3-41, 3-42, 3-43, 3-67, and 3-68 (1996).

EXAMINER

Ken S. Kim

DATE CONSIDERED

7-9-04

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.

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EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB- CLASS	FILING DATE
	AA3						
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	AL3						Yes No
	AM3						Yes No
	AN3						Yes No
	AO3						Yes No
	AP3						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

CSK	AR	3	Diefendorff, Keith, <i>et al.</i> , "Organization of the Motorola 88110 Superscalar RISC Microprocessor," <i>IEEE Micro</i> , 12:2, pp. 40-63 (April 1992).
CSK	AS	3	Diefendorff, Keith, <i>et al.</i> , "Altivec Extension to PowerPC Accelerates Media Processing," <i>IEEE Micro</i> , 20:2, pp. 85-95 (March-April 2000).
CSK	AT	3	ESA/390 Principles of Operation, IBM Library Server, Table of Contents and Paragraphs 7.5.31 and 7.5.70, 23 pages, at http://publibz.boulder.ibm.com/cgi-bin/bookmgr_OS390/BOOK/DZ9AR001/CCONTENTS (1993).

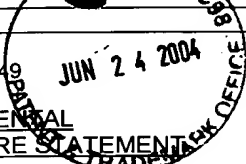
EXAMINER

Ken S. Klein

DATE CONSIDERED

7-9-04

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			FIRST NAMED INVENTOR Kevin D. Kissell		
			FILING DATE June 18, 2001	ART UNIT 2111	

U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
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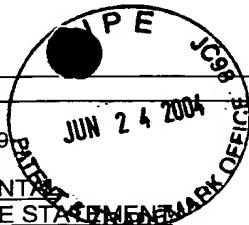
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EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AL4						Yes No
	AM4						Yes No
	AN4						Yes No
	AO4						Yes No
	AP4						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)			
(CS)C	AR	4	Kane, Gerry, <i>PA-RISC 2.0 Architecture</i> , Prentice Hall PTR, Upper Saddle River, New Jersey, pp. 7-106 and 7-107 (1996).
(CS)K	AS	4	May, Cathy, et al., eds., <i>The PowerPC™ Architecture: A Specification for a New Family of RISC Processors</i> , Second Edition, Morgan Kaufmann Publishers, Inc., San Francisco, California, pp. 70-72 (May 1994).
(CS)K	AT	4	MC68020 32-Bit Microprocessor User's Manual, Third Edition, Prentice Hall, Englewood Cliffs, New Jersey, pp. 3-125, 3-126, and 3-127 (1989).

EXAMINER <u>Ken S. Kim</u>	DATE CONSIDERED <u>7-9-04</u>
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U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	AA5						
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	AL5						Yes No
	AM5						Yes No
	AN5						Yes No
	AO5						Yes No
	AP5						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)			
Csk	AR	5	MC88110 Second Generation RISC Microprocessor User's Manual, Motorola, Inc., pp.10-66, 10-67, and 10-71 (1991).
Csk	AS	5	Nemirovsky, Mario, et al., U.S. Application Number 09/629,805; entitled "Method and Apparatus for Improved Computer Load and Store Operations," filed July 31, 2000, 41 pages.
	AT	5	

EXAMINER: <u>Ken S. Kissell</u>	DATE CONSIDERED: <u>7-7-04</u>
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ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of
Invention

Method and Apparatus for Saving and Restoring Processor
Register Values and Allocating and Deallocating Stack
Memory

Application Number: 09/882285 ✓

Confirmation Number: 7899

First Named Applicant: Kevin KISSELL

Attorney Docket Number: 1778.0210000

Art Unit: 2111

Examiner: Kenneth S. Kim

Search string: (4338663 or 4833640 or 5031096 or 5109495
or 5241679 or 5327566 or 5442761 or 5493687
or 5517664 or 5524211 or 5542060 or 5574928
or 6212630 or 20020138715).pn.



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US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
1536	1	4338663	1982-07-06	Strecker et al.		712	228
1536	2	4833640	1989-05-23	Baba		718	108
1536	3	5031096	1991-07-09	Jen et al.		711	169
1536	4	5109495	1992-04-28	Fite et al.		712	207
dup	5	5241679	1993-08-31	Nakagawa et al.			
1536	6	5327566	1994-07-05	Forsyth		710	260
1536	7	5442761	1995-08-15	Toda et al.		712	205
1536	8	5493687	1996-02-20	Garg et al.		712	23
dup	9	5517664	1996-05-14	Watanabe et al.			
1536	10	5524211	1996-06-04	Woods et al.		709	220
1536	11	5542060	1996-07-30	Yoshida		712	208
1536	12	5574928	1996-11-12	White et al.		712	23
dup	13	6212630	2001-04-03	Takayama et al.	B1		

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7-9-04



US Published Applications

09/882285

Note: Applicant is not required to submit a paper copy of cited US Published Applications

init	Cite.No.	Pub. No.	Date	Applicant	Kind	Class	Subclass
dup	1	20020138715	2002-09-26	Minematsu	B1		

Remarks

Note: Remarks are not for responding to an office action.

This IDS is being filed after the receipt of an Ex Parte Quayle Action. However, this IDS is being filed the same day as a Request for Continued Examination under 37 CFR 1.114. Therefore, no statement or fee is required.

Signature

Examiner Name	Date
Ken S. Kim	7-9-04

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